

Ph.D. Viva-Voce Examination Report

Name of the Scholar	Ms.Nagavarapu Sowmya
Registration No	19SPHEC030
Name of Department:	Electronics and Communication Engineering
School:	School of Engineering and Technology
Title of Thesis:	FPGA Implementation of Neuromorphic Processor and Molecular Docking Analysis in Bio-Medical Application
Supervisor	Dr. Jitendra Kumar Assistant Professor, Dept. of Electronics and Communication Engineering. School of Engineering & Technology GIET University, Gunupur
Co-Supervisor	Dr. Pradyut Kumar Biswal Assistant Professor Dept. of Electronics and Communication Engineering, IIT,Bhubaneswar
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DRC Member 2	Dr. Priyadarsan Parida Associate Professor Department of Electronics and Engineering, GIET University
DRC Member 3	Dr. Ami Kumar Parida Department of Electronics and Communication Engineering, GIET University
External Examiner	Dr.M Manikandan Professor, Department of Computer Science and Engineering MIT Campus, Anna University
Status	Recommended to Award the degree
Date	04/01/2025
Number of offline attendee	20
Number of online attendee	28



